

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Tatsuya Arao et al. Art Unit : Unknown
Serial No. : New Divisional Application Examiner : Unknown
Filed : July 21, 2003
Title : METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

Attention: Official Draftsman

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF FORMAL DRAWINGS

Please substitute the enclosed twenty-five (25) sheets of formal drawings (Figs. 1A-25C) for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: July 21, 2003



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